

<b>Notice of References Cited</b>	Application/Control No. 10/534,411		Applicant(s)/Patent Under Reexamination TSUYUMOTO ET AL.	
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#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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#### FOREIGN PATENT DOCUMENTS

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#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Ikeda Kenichi et al., "Wiring Board Prepreg and Its Manufacturing Method", Machine translation of JP 2002-208781, Application Publication Date 26 July 2002.			
	V				
	W				
	X				

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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